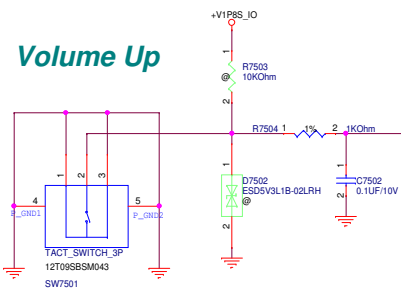
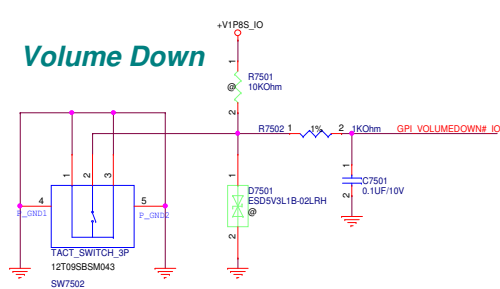


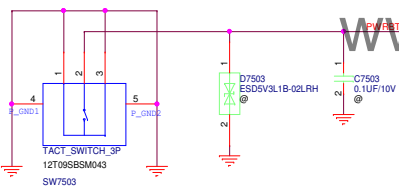
Volume Up



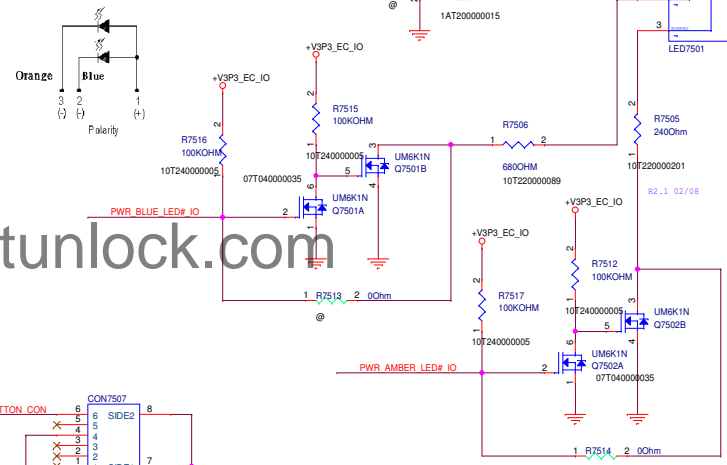
Volume Down



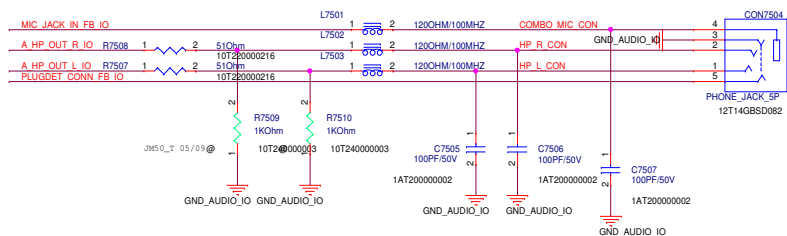
Power Key



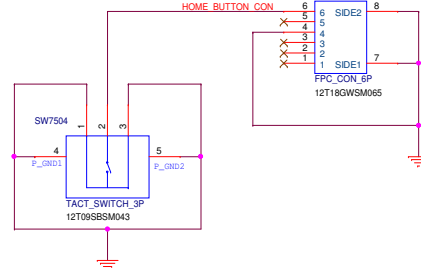
POWER LED



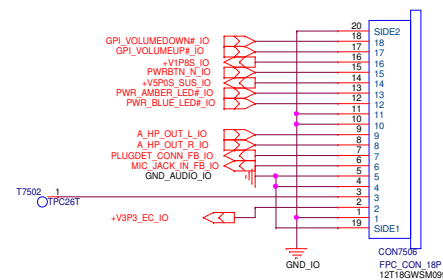
AUDIO COMBO JACK



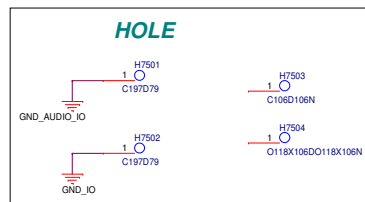
Home Key



AUDIO BD TO MB



HOLE



Core Design

PEGATRON Title : AUDIO BD

PEGATRON CONFIDENTIAL Engineer: Shan_Wu

Size Project Name LongChamp

Date: Thursday, July 10, 2014 Sheet 1 of 1

Rev 1.1

to LVDS Panel connector

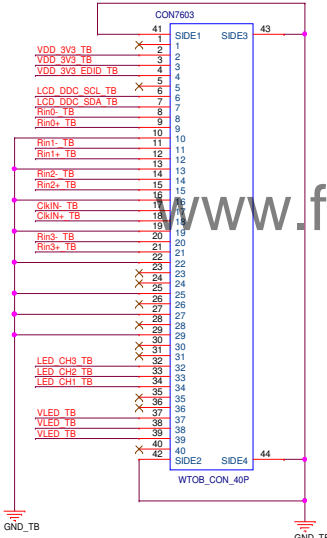
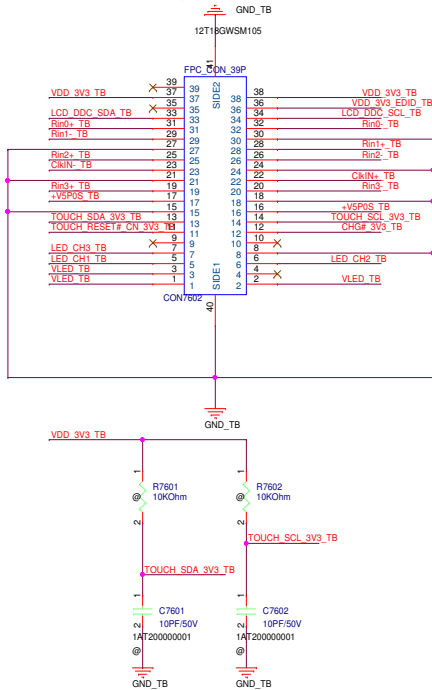
to touch board connector

LVDS 39pin connector (From MB)

LVDS Transform board to LVDS panel Longchamp LVDS Panel (AUO)

(LVDS connector)

1 NC
2 VCC
3 VCC
4 VEDID
5 NC
6 DDC CLK
7 DDC DATA
8 Rin0-
9 Rin0+
10 GND
11 Rin1-
12 Rin1+
13 GND
14 Rin2-
15 Rin2+
16 GND
17 CLKIN-
18 CLKIN+
19 GND
20 Rin3-
21 Rin3+
22 GND
23 VTSP 5V
24 VTSP 5V
25 GND
26 I2C_CLK
27 I2C_DATA
28 INT
29 REST#
30 NC
31 NC
32 VCD4
33 VCD3 (CH3)
34 VCD2 (CH2)
35 VCD1 (CH1)
36 NC
37 VLED
38 VLED
39 VLED



1 NC
2 VCC
3 VCC
4 VEDID
5 AGING
6 CLK_EDID
7 DAT_EDID
8 Rin0-
9 Rin0+
10 GND
11 Rin1-
12 Rin1+
13 GND
14 Rin2-
15 Rin2+
16 GND
17 CLKIN-
18 CLKIN+
19 GND
20 Rin3-
21 Rin3+
22 GND
23 NC
24 NC
25 GND
26 NC
27 GND
28 NC
29 GND
30 NC
31 NC
32 VCD3 (CH3)
33 VCD2 (CH2)
34 VCD1 (CH1)
35 NC
36 NC
37 VLED
38 VLED
39 VLED
40 NC

